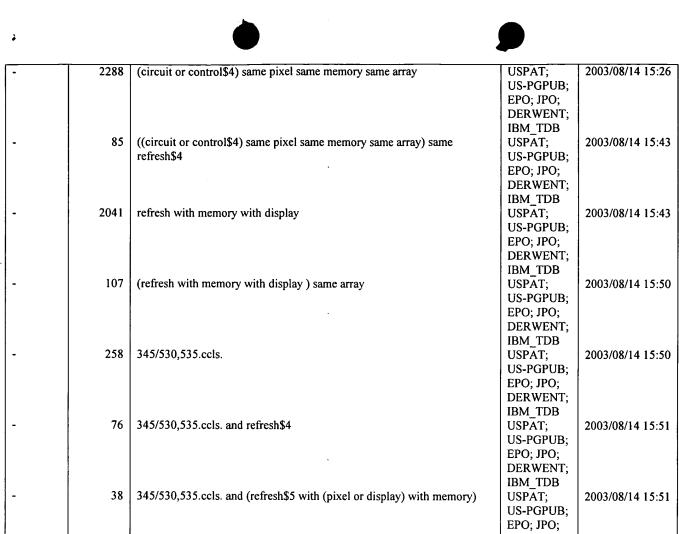
US-PCPUB; EPC; IPC; DERWENT; IBM_TDB USPAT; US-PCPUB; EPC; IPC; DERWENT; IBM_TDB U	•				
FPC; PPC; DERWENT; IBM_TDB USPAT; US-PCPUB; EPC; PPC; DERWENT; IBM_TDB USPAT; US-PCPUB; EPC; PPC; PPC; DERWENT; IBM_TDB USPAT; US-PCPUB; EPC; PPC; PPC; PPC; PPC; PPC; PPC; PPC	-	0	refresh same (pixel adj array) same (memory adj array)		2003/04/14 10:46
DERWENT; IBM_TDB USPAT; US-PGPUB; EPC; IPC; DERWENT; IBM_TDB USPAT; US-PGPUB; EPC;					
18M_TDB 18M_					
Common					
U.S.P.GPUB; EFO, JPO; DERWENT; IBM TIDB USPAT; U.S.P.GPUB; EFO, JPO; DERWENT; ISM TIDB USPAT; U.S.P.GPUB; EFO, JPO; D				IBM_TDB	
FPO, IPO, DERWENT, IBM, TDB USPAT; US-PGPUB, EPO, IPO, DERWENT,	-	0	refresh\$ same (pixel adj array) same (memory adj array)	USPAT;	2003/04/14 10:46
DERWENT BIM TIDB USPAT USPGPUB; EFO, IPO, DERWENT BIM TIDB USPAT	-			US-PGPUB;	
DERWENT BIM TIDB USPAT USPGPUB; EFO, IPO, DERWENT BIM TIDB USPAT				EPO; JPO;	
- 160 refresh\$ same (pixel) same (memory)same array					
- 160 refresh\$ same (pixel) same (memory)same array	1			IBM TDB	
US-PGPUB; EPO, IPO, DERWENT; BRATT, US-PGPUB; EPO, IPO, DERWENT; US-PGPUB; EPO, IPO, DERW	i -	160	refresh\$ same (pixel) same (memory)same array	_	2003/04/14 10:46
-			, , , , , , , , , , , , , , , , , , , ,		
DERWENT; IBM_TDB USPAT; USP-QPUB; EPO; IPO; DERWENT; USP-QPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; USP-QPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; USP-QPUB; EPO; IPO; DERWENT; US		·			
IBM_TDB USPAT; USP-GPUB; EPO, IPO; DERWENT; IBM_TDB USPAT; USP-GPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; USP-GPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; USP	İ				
2521 refresh\$5 with (pixel or display) with memory				·	
US-PGPUB: EPO, IPO; DERWENT; IBM TDB USPAT; US-PGPUB: EPO, IPO; DERWENT; US-PGPUB: EPO; I	_	2521	refresh\$5 with (nixe) or display) with memory		2003/08/14 14:22
-		2321	Teneshap with (pixel of display) with memory		2003/00/14 14.22
DERWENT; IBM TOB USPAT; US-PGPUB; EPO; IPO; DERWENT; IBM TOB USPAT; US-PGPUB; IBM TOB USPAT; US-PGPUB; IBM TOB USPAT; US-PGPUB					
151 (refresh\$5 with (pixel or display) with memory) same array					
151 (refresh\$5 with (pixel or display) with memory) same array					
US-PGPUB; EPO, JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO, JPO; DERWENT; IBM_TDB USPAT;		151	(refrech\$5 with (nivel or display) with manage)		2002/09/14 14:22
EPC, IPC, DERWENT;	-	151	(Terreshab) with (pixel of display) with memory) same array		2003/08/14 14:22
- 73 (refresh\$5 with (pixel or display) with memory) with array - 74 (refresh\$5 with (pixel or display) with memory) with array - 75 (refresh adj circuit) same memory same pixel same array - 76 (refresh adj circuit) same memory same pixel same array - 77 (refresh adj circuit) same memory same pixel same array - 78 (refresh adj circuit) same pixel adj array) same refresh\$5 - 79 (memory adj array same pixel adj array) same refresh\$5 - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 71 (memory adj array same pixel adj array) - 71 (memory adj array same pixel adj array) - 71 (memory adj array same pixel adj array) - 72 (memory adj array same pixel adj array) - 73 (memory adj array same pixel adj array) - 74 (memory adj array same pixel adj array) - 75 (memory adj array same pixel adj array) - 75 (memory adj array with pixel adj array) - 76 (memory adj array same pixel adj array) - 77 (memory adj array same pixel adj array) - 78 (memory adj array same pixel adj array) - 79 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj array) - 70 (memory adj array same pixel adj ar					
- 73 (refresh\$5 with (pixel or display) with memory) with array USPAT; US-PGPUB; EPO, IPO, DERWENT; IBM, TDB USPAT; USPA					
- 1384 (refresh\$5 with (pixel or display) with memory) with array USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB					
US-PGPUB; EPO; IPO; DERWENT; IBM, TDB USPAT; US-PGPUB; EPO; IPO; IPO; IPO; IPO; IPO; IPO; IPO; I	ł				
Company Comp	-	73	(refresh\$5 with (pixel or display) with memory) with array		2003/08/14 14:53
Company Comp				-	
BBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPAT; US-PG					
- 4 (refresh adj circuit) same memory same pixel same array USP-AT; USP-GPUB; EPO; IPO; DERWENT; IBM_TDB USP-AT; US-PGPUB; EPO; IPO; DERWENT; IBM_TDB USP-AT; USP-GPUB; EPO; IPO; DERWENT; IBM_TDB				DERWENT;	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO;					
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; I	-	4	(refresh adj circuit) same memory same pixel same array	USPAT;	2003/08/14 14:55
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; E					
BM_TDB					
- 0 (memory adj array same pixel adj array) same refresh\$5 USPAT; US-PGPUB; EPO; PO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; PO; PO; PO; PO; PO; PO; PO; PO; PO;				DERWENT;	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO;				IBM_TDB	
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB EPO; JPO; DERWENT; EPO; J	-	0	(memory adj array same pixel adj array) same refresh\$5		2003/08/14 14:56
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPO; JPO; DERWENT; JPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO					
BM_TDB					
- 19 memory adj array same pixel adj array USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO;					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO;				IBM_TDB	
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; D	-	52	memory adj array same pixel adj array		2003/08/14 14:56
- 19 memory adj array with pixel adj array memory adj array with pixel adj array per					
19 memory adj array with pixel adj array IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; D					
- 19 memory adj array with pixel adj array USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPO; JPO; DERWENT; JPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO					
Table Ferrority Ferrorit	-	19	memory adj array with pixel adj array		2003/08/14 15:15
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB EPO; JPO; DERWENT; IBM_TDB EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; DERWENT; DERWENT; EPO; JPO; EPO; JPO; DERWENT; EPO; JPO; EPO; JPO; EPO; EPO; JPO; EPO;	1				
- 1384 refresh adj circuit				EPO; JPO;	
- 1384 refresh adj circuit				DERWENT;	
- 1384 refresh adj circuit - 8 (refresh adj circuit) same pixel same memory - 8 (refresh adj circuit) same pixel same memory - 2 5128658.pn. USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;				IBM_TDB	
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; USPAT;	-	1384	refresh adj circuit	USPAT,	2003/08/14 15:16
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; USPAT;			·	US-PGPUB;	
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; USPAT;					
- 8 (refresh adj circuit) same pixel same memory IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	1				
- 8 (refresh adj circuit) same pixel same memory USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	-	8	(refresh adj circuit) same pixel same memory		2003/08/14 15:20
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT; DERWENT;	1		•		
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT;					
- 2 5128658.pn. IBM_TDB					
- 2 5128658.pn. USPAT; US-PGPUB; EPO; JPO; DERWENT;					
US-PGPUB; EPO; JPO; DERWENT;	1 -	2	5128658.pn.		2003/08/14 15:25
EPO; JPO; DERWENT;			•		
DERWENT;	1				
	1			IBM TDB	



			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	l
-	85	((circuit or control\$4) same pixel same memory same array) same	USPAT;	2003/08/14 15:43
		refresh\$4	US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2041	refresh with memory with display	USPAT;	2003/08/14 15:43
	İ		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	107	(refresh with memory with display) same array	USPĀT;	2003/08/14 15:50
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	258	345/530,535.ccls.	USPĀT;	2003/08/14 15:50
		,	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	76	345/530,535.ccls. and refresh\$4	USPAT;	2003/08/14 15:51
	, ,	2 13/23 0,030. Wild Policelly 1	US-PGPUB;	2003/00/14 13.31
	i e		EPO; JPO;	
		•	DERWENT;	
	ı		IBM TDB	
l _	38	345/530,535.ccls. and (refresh\$5 with (pixel or display) with memory)	_	2003/08/14 15:51
-	36	343/330,333.ecis. and (terreshab with (pixel of display) with memory)	USPAT;	2003/08/14 13:31
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	0	(mamony adi amony) gama (diantay an nival) adi2 amony anno nafarah a64	IBM_TDB	2002/00/21 00:50
-	0	(memory adj arrary) same (display or pixel) adj2 arrary same refreshe\$4 same (cycle or tim\$4)	USPAT	2003/08/21 09:58
	0		LICDATE	2002/00/21 00 50
_	829	(memory adj arrary) same (display or pixel) adj2 arrary same refreshe\$4 (memory) same (display or pixel) same refreshe\$4	USPAT	2003/08/21 09:58
1 -	115		USPAT	2003/08/21 09:58
-	7	((memory)) same (display or pixel) same refreshe\$4) same cycle	USPAT	2003/08/21 10:35
1 -	/	dram adj refresh\$4 same (memory adj control\$4) same (display adj	USPAT	2003/08/21 10:44
İ	51	controll\$4)	TIOD AT	2002/00/21 10 11
_	51	DRAMadj refresh\$4 same (memory adj control\$4) same (display adj	USPAT	2003/08/21 10:44
	_	controll\$4)	Hant	2002/00/21 12 1
-	7	DRAM adj refresh\$4 same (memory adj control\$4) same (display adj	USPAT	2003/08/21 10:45
	4.5	controll\$4)	LIOD A.T.	2002/00/21 12:-
-	46	(DRAM adj2 refresh\$5) same (memory) same (pixel or display) same	USPAT	2003/08/21 10:45
	97610	control\$5	LIOD 4 TO	0004/00//
-	87619	(dynamic adj random adj access adj memory) or DRAM	USPAT;	2004/03/16 10:54
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
İ	0000	//	IBM_TDB	
-	9898	((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 10:55
1		(refresh\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	533	(((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 10:56
		(refresh\$4)) same (memory) same (pixel or display)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
L			IBM_TDB	
				

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4	

	379	((((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 11:32
	3/9	(((dynamic adj random adj access adj memory) of <i>Dready</i>) same (refresh\$4)) same (memory) same (pixel or display)) and @ad<19990730	US-PGPUB;	2004/03/10 11.32
		(Terreship4)) same (memory) same (pixer or display)) and (agad 1777)0750	EPO; JPO;	
			DERWENT;	
			IBM TDB	
	431	refresh near3 (control\$4 or circuit) with (memory) with (pixel or display)	USPAT:	2004/03/16 11:33
		(,,,,	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
'			IBM TDB	
-	233	(refresh near3 (control\$4 or circuit) with (memory) with (pixel or	USPĀT;	2004/03/16 11:35
		display)) and @ad<19900730	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	(refresh near3 (control\$4 or circuit) with (memory) with (pixel or	USPAT;	2004/03/16 12:21
		display)) same DRAM	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	2255		IBM_TDB	
-	2357	(pixel with memory) with array	USPAT;	2004/03/16 12:21
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	24	((pixel with memory) with array) same (refresh\$4 near3 (circuit or	IBM_TDB	2004/03/16 13:12
-	24	control\$4))	USPAT; US-PGPUB;	2004/03/16 13:12
		Control (\$47)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	4	((pixel with memory) with array) with refresh\$4 near3 (circuit or	USPAT;	2004/03/16 13:13
		control\$4)	US-PGPUB:	2007/05/10 15.15
		· '	EPO; JPO;	
			DERWENT;	
			IBM TDB	



L Number	Hits	Search Text	DB	Time stamp
1	87619	(dynamic adj random adj access adj memory) or DRAM	USPAT;	2004/03/16 10:54
			US-PGPUB;	
			EPO; JPO;	
		•	DERWENT;	
			IBM_TDB	
2	9898	((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 10:55
		(refresh\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
3	533	(((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 10:56
		(refresh\$4)) same (memory) same (pixel or display)	US-PGPUB;	
			EPO; JPO;	
		,	DERWENT;	
			IBM_TDB	
4	(379)	((((dynamic adj random adj access adj memory) or DRAM) same	USPAT;	2004/03/16 11:32
		(refresh\$4)) same (memory) same (pixel or display)) and @ad<19990730	US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
5	431	refresh near3 (control\$4 or circuit) with (memory) with (pixel or display)	USPAT;	2004/03/16 11:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
6	233	(refresh near3 (control\$4 or circuit) with (memory) with (pixel or	USPAT;	2004/03/16 11:35
		display)) and @ad<19900730	US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
1			IBM_TDB	
7	20	(refresh near3 (control\$4 or circuit) with (memory) with (pixel or	USPAT;	2004/03/16 11:35
		display)) same DRAM	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	



L Number	Hits	Search Text	DB	Time stamp
1	183	refresh adj3 (control\$4 or circuit) with (pixel or display) with memory	USPAT	2004/03/16 13:30
2	13	(refresh adj3 (control\$4 or circuit) with (pixel or display) with memory) same DRAM	USPAT	2004/03/16 14:10
3	3691	refresh adj3 (circuit or control\$4)	USPAT	2004/03/16 14:10
4	183	(refresh adj3 (circuit or control\$4)) with memory with (pixel or display)	USPAT	2004/03/16 14:11
5	169	((refresh adj3 (circuit or control\$4)) with memory with (pixel or display)) and @ad<19990730	USPAT	2004/03/16 15:06
6	2574	refresh adj (circuit or control\$4)	USPAT	2004/03/16 15:07
7	68	(refresh adj (circuit or control\$4)) with (pixel or display) with memory	USPAT	2004/03/16 15:07
8	61	((refresh adj (circuit or control\$4)) with (pixel or display) with memory) and @ad<19990730	USPAT	2004/03/16 15:07